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Description:

The MC2300 is a motion control processor for brushless servo motors and provides one to four axes of motion. This document details bug fixes and changes for this release.

Version 2.4

Known Issues:

If the IO chip HostRdy signal (pin 8) is used for chip busy detection, the first instruction sent to the chipset after a power on or reset may be ignored or may produce a checksum error. It is recommended that in this configuration a NoOperation command be sent to the chipset as the first instruction after a power on or reset. If the *ReadStatus* operation is used to check the HostRdy state this problem does not occur.

To determine if the motion processor has generated an interrupt to the host, the host should check the state of the host interrupt signal. In most situations this signal should be connected to an external interrupt input on the host processor. The state of this signal is also reported by the *ReadStatus* operation but it is only accurate when the host is issuing actual chip commands and not simply polling *ReadStatus*.

Known Bugs:

none

Changes/Fixes:

Command Changes

none

Profile Changes

Resolved a bug in the SCurve profile that resulted in an overshoot in phase 1 of the profile for extremely short moves. This would in turn create an error in the trajectory in Phase 5/6, which caused the trajectory to overshoot but settle at the correct destination.

Resolved a bug in the SCurve profile that would cause the trajectory to automatically restart if a ClearPositionError command was given after a move in the negative direction trajectory completed.

Resolved a bug in Velocity Contouring profile mode that could cause the motion complete bit to not be set if the axis was switched into VC on-the-fly from either SCurve or Trapezoidal profile mode.

Registers and Signals Changes

none

Miscellaneous Changes

Resolved an issue with analog inputs where the default conversion timing resulted in out of specification results. This has been fixed.

Resolved an issue with the wrong number of microsteps being generated when an axis was set to microstepping commutation.

Version 2.3

Known Issues:

If the IO chip HostRdy signal (pin 8) is used for chip busy detection, the first instruction sent to the chipset after a power on or reset may be ignored or may produce a checksum error. It is recommended that in this configuration a NoOperation command be sent to the chipset as the first instruction after a power on or reset. If the *ReadStatus* operation is used to check the HostRdy state this problem does not occur.

Known Bugs:

When SetInterruptMask is non-zero and the corresponding EventStatus bit is set, parallel host to I/O communications can become corrupted. The corruption causes the second and subsequent data reads to equal the first. If checksums are being read a checksum error will occur. If 32-bit values are being read the second word will equal the first which may result in unexpected data. If only 16-bit words are being read without checksum verification no errors will occur. This is the case with a normal interrupt service routine which sends GetInterruptAxis, GetEventStatus and ResetEventStatus.

If an Update or MultiUpdate is issued that would start the external profile motion AND a limit switch is currently active it will cause a chip reset. The workaround is to use any other profile mode to first move out of the limit before starting the external profile mode motion. If the chip enters a limit when the axis is already in external profile mode the chip behaves as expected, stopping motion if the LimitSwitchMode is set to enabled.

Changes/Fixes:

Command Changes

none

Profile Changes

none

Registers and Signals Changes

none

Miscellaneous Changes

When an axis had its encoder source set to parallel, any axes that followed it (1,2,3 or 4) and had an incremental encoder source would read an incorrect value. This is now fixed.
When an analog input channel was stored using the trace facility, the value was incorrectly stored as a signed instead of unsigned value. This is now fixed.

Version 2.2

Known Bugs:

none

Changes/Fixes:

Command Changes

none

Profile Changes

Resolved an issue that prevented External Profile Mode from operating correctly with synchronous RAM.

Registers and Signals Changes

none

Miscellaneous Changes

none

Version 2.1

Known Bugs:

none

Changes/Fixes:

Command Changes

none

Profile Changes

Changes made to External Profile Mode as detailed below for compatibility with the Pathfinder contouring library.
External profile mode now responds correctly to limit switches. It also responds correctly to an AbruptStop set through a breakpoint or through a SetStopMode AbruptStop command. SmoothStop has no effect when the chipset is in external profile mode.
External profile mode now stops if it encounters an entry in the time buffer where the value is zero. This is a more convenient way of halting this profile mode than using a time or motion complete based breakpoint.

Registers and Signals Changes

none

Miscellaneous Changes

none

Version 2.0

Known Bugs:

none

Changes/Fixes:

Command Changes

none

Profile Changes

none

Registers and Signals Changes

none

Miscellaneous Changes

Version 2.0 and above of this chipset use a new IO that facilitates higher parallel communication speed. There is now no additional CP overhead if the checksum is read. "Get" commands can see a speed improvement of up to 30%.

In multi-drop serial mode, the chip previously did not respond with the expected status packet when a software "Reset" command was executed. This is now fixed.